**Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-4. **(Withdrawn).**

5. **(Currently Amended)** An apparatus for interfacing between first and second successive stages of an ASIC (Application Specific Integrated Circuit) design ~~synthesis tool~~, said apparatus comprising:

a processor,

a plurality of selectors coupled to the processor and each corresponding to a tool for performing a stage in an ASIC design, and

a memory coupled to the processor and storing therein respective script files relating to each of said selectors;

said processor being responsive to selection of at least one of the selectors for accessing the memory and executing commands associated therewith;

wherein the tool for performing a stage in the ASIC design is one of a DFT (Design For Test) unit, a static verification unit, a physical compile unit, a physical STA (Static Timing Analysis) unit and an ECO (Electrical Change Order) unit and wherein the tool is not an ASIC synthesis tool.

6. **(Previously Presented)** The apparatus according to Claim 5, wherein the commands executed by the processor are extracted from the respective script file relating to each of said selected selectors.

7. **(Previously Presented)** The apparatus according to Claim 6, wherein the script file relating to each of said selected selectors is automatically generated.

8. **(Previously Presented)** The apparatus according to Claim 6, wherein the script file relating to each of said selected selectors is automatically generated according to a pre-prepared template.

9. **(Previously Presented)** The apparatus according to Claim 8, wherein the pre-prepared template is part of the selected selector.

10. **(Previously Presented)** The apparatus according to Claim 8, wherein the pre-prepared template is a pre-prepared external file.

11. **(Previously Presented)** The apparatus according to Claim 10, wherein the pre-prepared external template file is an XML file.

12. **(Currently Amended)** The apparatus according to Claim 5, wherein the commands executed by the processor are ~~parts of~~ related to each of said selected selectors.

13. **(Previously Presented)** The apparatus according to Claim 5, wherein at least some of said selectors are operable via a graphical user interface.

14-15. **(Withdrawn)**

16. **(New)** The apparatus according to Claim 5, wherein the processor is adapted to provide automatic integration with version control tools.